Page 13, line 16, change "Figure 2" to --Figures 2a-2d--.

Page 15, line 12, cancel "voltages" and substitute --voltage--.

Page 16, line 10, change "stand by" to --standby--.

IN THE CLAIMS

1.   (Amended)   A dynamic power management device for supplying power to a solid state memory integrated circuit in an electronic system, said device comprising:

power control means for supplying a variable voltage to said memory integrated circuit independently from the voltage supplied to the rest of said system; and

logic control means [responsive to data access activity] for generating address and control signals for said memory integrated circuit and for controlling said power control means to supply power to said memory integrated circuit [sufficient] at a level to maintain memory information in said memory integrated circuit during periods of no data access activity and supplying power at a level [sufficient] to exchange memory information with said memory integrated circuit during periods of data access activity, said power supplied during periods of no data access activity being less than said power supplied during periods of data access activity, whereby power consumption of said memory integrated circuit is curtailed.

2.   (Amended)   The dynamic power management device [apparatus] of Claim 1, further comprising I/O means for converting parallel data signals to serial data signals for input to said memory integrated circuit and for converting data signals output from said memory integrated circuit from serial to parallel [and performing error correction on the resulting parallel data signals].